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CORDIC algorithm. 3. The Basic Cordic Algorithm Volder [1] implemented the basic CORDIC algorithm for multiplication, division, conversion of binary to decimal and mixed radix number systems. Walther [2] generalised the techniques proposed by

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Folder in order to compute hyperbolic, exponential, logarithm and square root functions. Numerous ...

A Novel Method for Computing Exponential Function Using ...

Indeed, CORDIC has involved into a very rich and valuable area and until now, after more than 50 years, this algorithm

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is still an attractive algorithm. It can be used as single function unit named as CORDIC processor to generate large class of applications which include: trigonometric, hyperbolic, logarithmic, square-root, complex division-multiplication and transcendental elementary functions.

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Novel design for a low-latency CORDIC algorithm for sine ...

1877-7058 ' 2011 Published by Elsevier
Ltd. doi: 10.1016/j.proeng.2012.01.893
Procedia Engineering 30 (2012) 519
â€” 528 Available online at
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Novel Method for Computing Exponential
Function Using CORDIC Algorithm J.
Sudha a , M. C Hanumantharaju b , V.
Venkateswarulu a , Jayalaxmi H c , a* ...

A Novel Method for Computing Exponential Function Using ...

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Exponential Function Using ... In this

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paper, we first classify the CORDIC algorithm based on the number system and discuss its importance in the implementation of ...

(PDF) A Novel Method for Computing Exponential Function ...

Novel design for a low-latency CORDIC algorithm for sine-cosine computation

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and its Implementation on FPGA This paper proposes a design of a fast FPGA based architecture for Coordinate Rotation Digital Computer (CORDIC) algorithm with reduced number of iterations. CORDIC is on such technique which uses just shift-add/sub operations.

Novel design for a low-latency

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CORDIC algorithm for sine ...

A NOVEL, OPTIMIZED CORDIC CORE FOR
PHASE CORRELATION MOTION
ESTIMATION Andrea Molino, Fabrizio
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Elettronica Politecnico di Torino @ Corso
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A NOVEL, OPTIMIZED CORDIC CORE FOR PHASE CORRELATION ...

per proposes a novel algorithm for vectoring mode of CORDIC which is totally scaling free with a provision for skipping iterations not actually needed so as to speed up the operation. Unlike the conventional Vectoring CORDIC, the rotation of vector in the proposed

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algorithm is always in one direction

A Novel Scaling free Vectoring CORDIC and its FPGA ...

A novel rotational VLSI architecture based on extended elementary angle set CORDIC algorithm Abstract: The CORDIC algorithm is a well-known iterative method for the computation of vector

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rotation. For applications that require forward rotation (or vector rotation) only, the angle recoding (AR) technique provides a relaxed approach to speed up the operation of the CORDIC algorithm.

A novel rotational VLSI architecture based on extended ...

The proposed algorithm and its first

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order architecture have been compared with the existing low latency CORDIC algorithms in terms of iterations, hardware complexity and critical delay. The scope of this work is to present a novel hybrid CORDIC algorithm along with first order hardware architecture.

Low Latency Hybrid CORDIC

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Algorithm - IEEE Journals & Magazine

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Ayan Banerjee, FPGA realization of a CORDIC based FFT processor for biomedical signal processing, Kharagpur, 2001; CORDIC Architectures: A Survey, B. Lakshmi and A. S. Dhar, Journal: VLSI Design, January 2010; Implementation of

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a CORDIC Algorithm in a Digital Down-
Converter, C. Cockrum, Fall 2008

Digital Circuits/CORDIC - Wikibooks, open books for an ...

CORDIC is an iterative algorithm for calculating trig functions including sine, cosine, magnitude and phase. It is particularly suited to hardware

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implementations because it does not require any multiplies. 1. Basics 1.1 What does "CORDIC" mean? COordinate Rotation Digital Computer. ... Continued

CORDIC FAQ - dspGuru

CORDIC (for COordinate Rotation Digital Computer), also known as Volder's algorithm, is a simple and efficient

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algorithm to calculate hyperbolic and trigonometric functions, typically converging with one digit (or bit) per iteration. CORDIC is therefore also an example of digit-by-digit algorithms. CORDIC and closely related methods known as pseudo-multiplication and pseudo-division or factor ...

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CORDIC - Wikipedia

The CORDIC algorithm allows the use of simple operators, such as adders and shift registers, ... A novel three-component reaction between isocyanides, isothiocyanates, ...

A novel design and implementation of FPGA based 3D-CORDIC ...

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A novel digital frequency synthesizer (DDS) is introduced using CORDIC algorithm module instead of ROM look-up table module in the paper.

Application of CORDIC algorithm module can greatly reduce the amount of storage and cancel the amount of storage to improve data accuracy and improve the DDS frequency resolution

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FPGA Implementation of a Novel Type DDS Based on CORDIC ...

NOVEL ALGORITHM FOR 8 POINT DCT &
IDCT IMPLEMENTATION BASED ON
CORDIC S.Ramesh,R.Kangeyan,
Department of ECE, Pallavan college of
engineering, Kanchipuram. ABSTRACT A

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novel coordinate rotation digital
computer (CORDIC)-based fast radix-2
algorithm for computation of discrete
cosine transformation (DCT) .

NOVEL ALGORITHM FOR 8 POINT DCT & IDCT IMPLEMENTATION ...

CORDIC Algorithm COordinate Rotation
Digital Computer • Method for

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elementary function evaluation (e.g., $\sin(z)$, $\cos(z)$, $\tan^{-1}(y)$) • The modern CORDIC algorithm was first described in 1959 by Jack E. Volder. It was developed to replace the analog resolver in the B-58 bomber's navigation computer. (from Wikipedia)

CORDIC Algorithm COordinate

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Rotation Digital Computer

CORDIC, developed by Volder [1] is a popular hardware-efficient algorithm that can be employed to compute the complex trigonometric functions in (1) and (2). It performs a series of micro-rotations on a vector lying on the X-Y plane over a desired input angle using simple add-shift operations. The CORDIC

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algorithm is

Copyright code:

[d41d8cd98f00b204e9800998ecf8427e](https://www.d41d8cd98f00b204e9800998ecf8427e).